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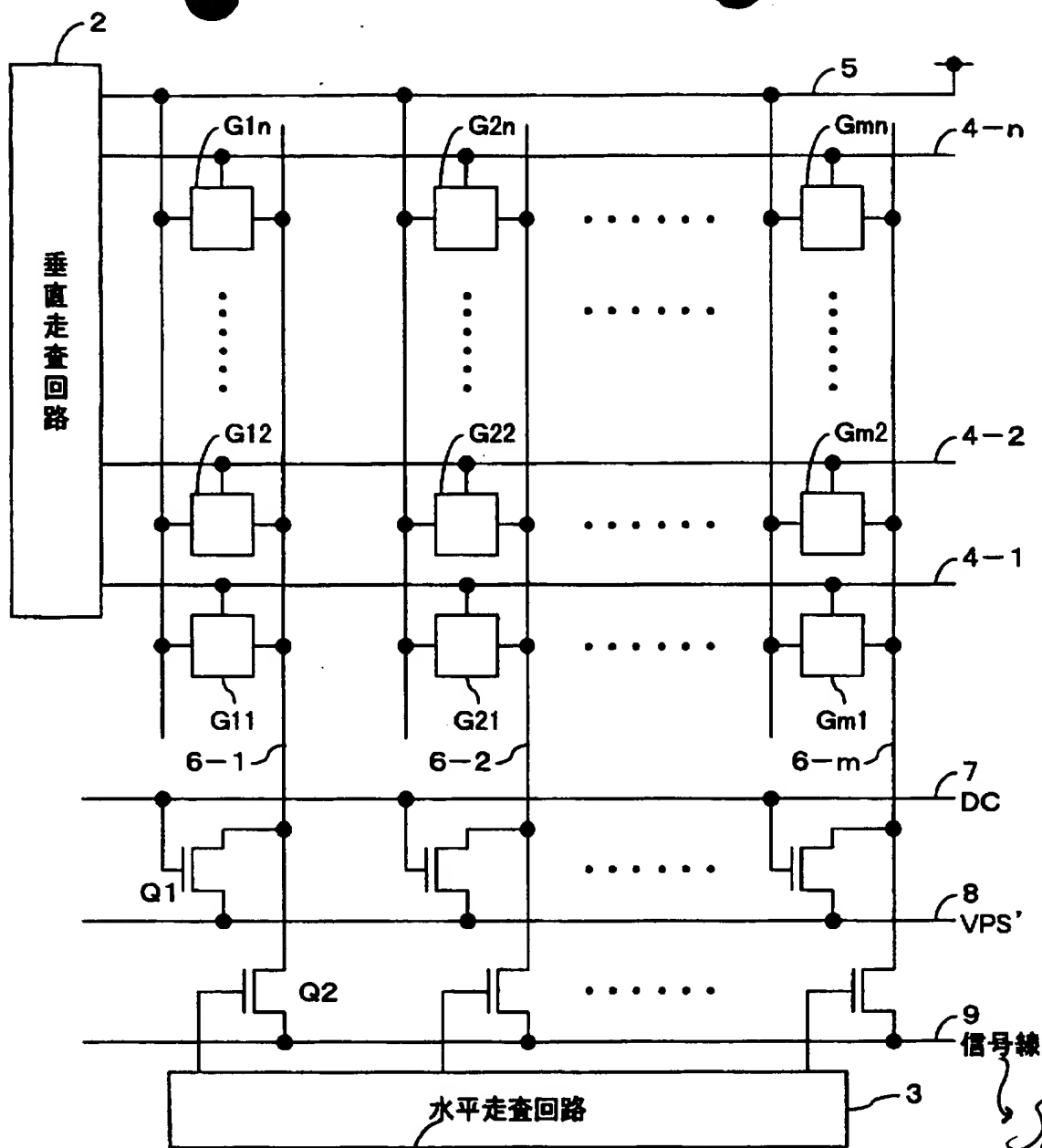


FIG. 1

Horizontal
Scanning
Circuit

Signal
line

FOOT 20" F25E0560

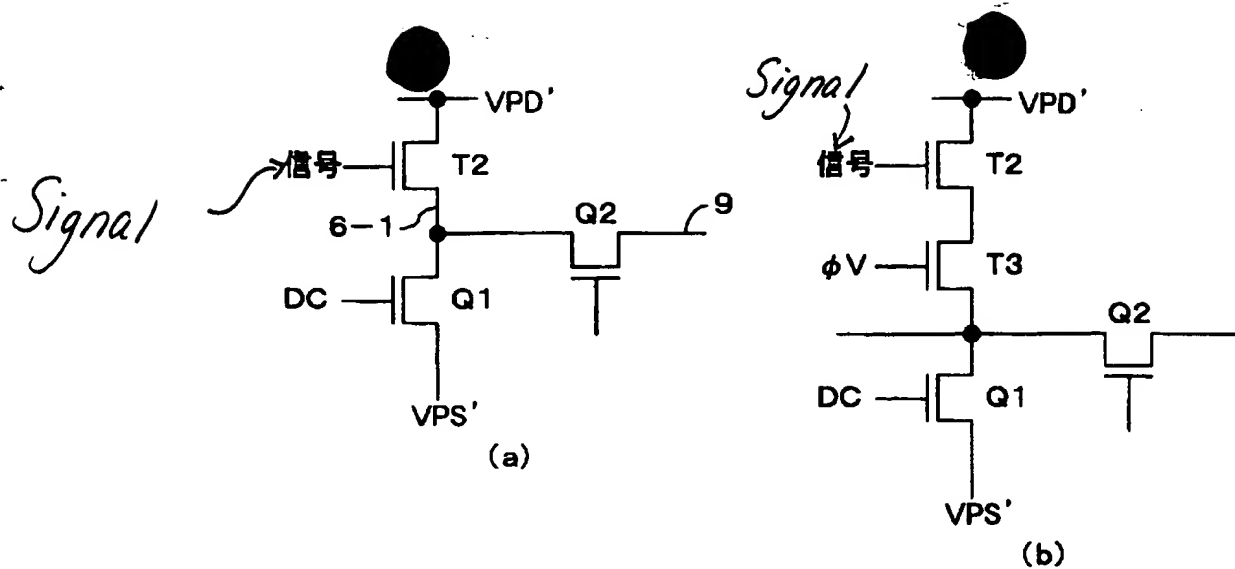


FIG. 2

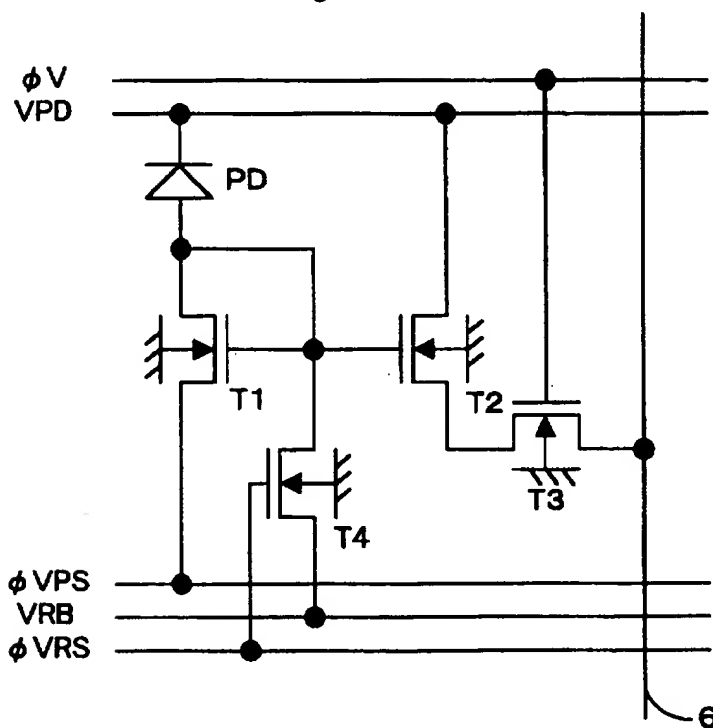


FIG. 3

Timing diagram showing the sequence of signals for the reset operation:

- ϕV : High Voltage signal (Hi/Low).
- ϕVPD : First reset pulse (labeled "First").
- ϕS : Second reset pulse (labeled "Second").
- Reset Period (リセット期間): The duration between the start of the first reset pulse and the start of the second reset pulse.

FIG. 5 \hookrightarrow Reset period

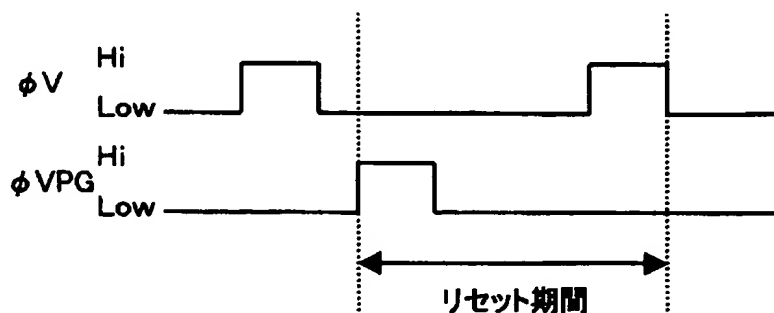
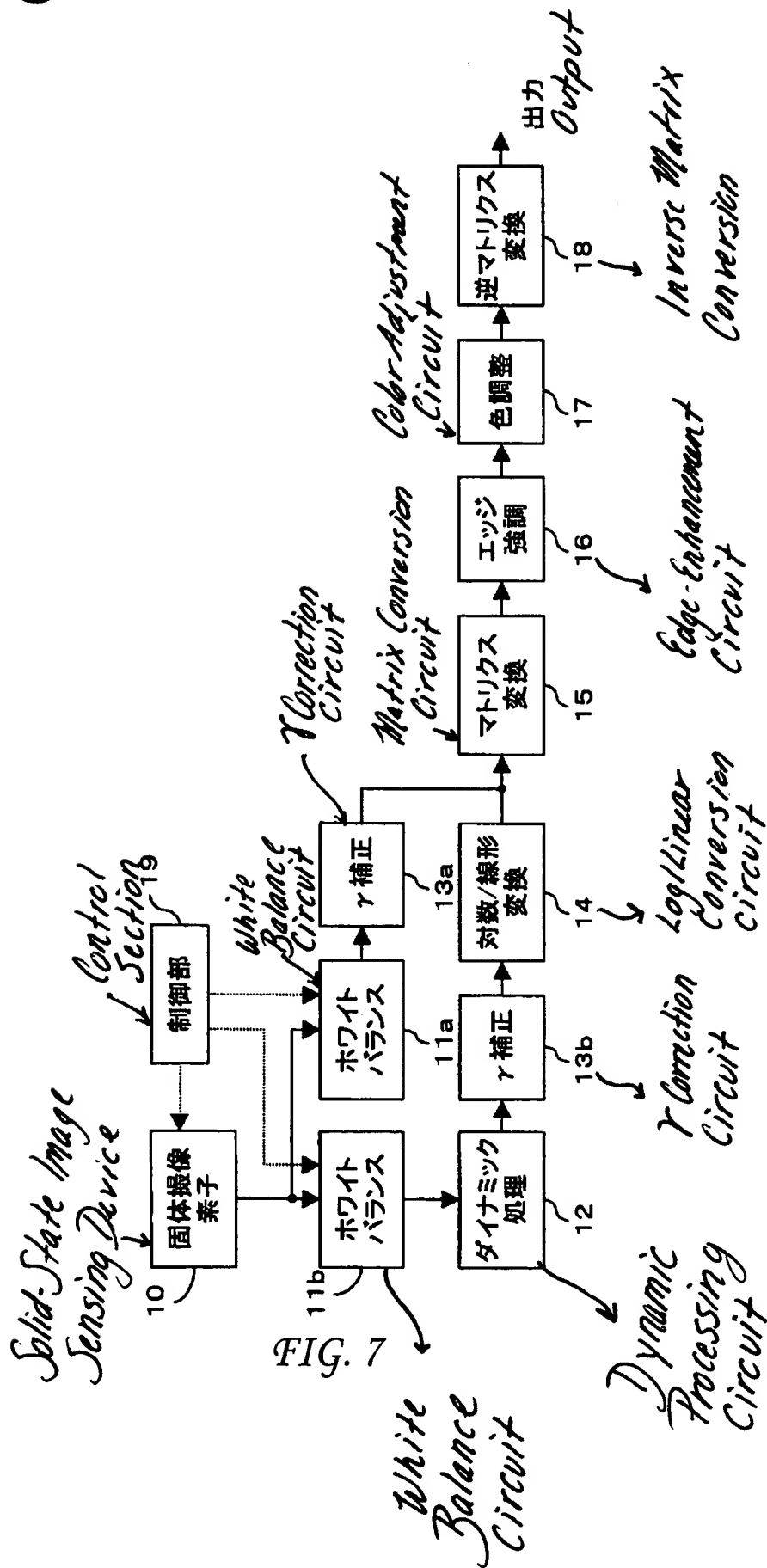
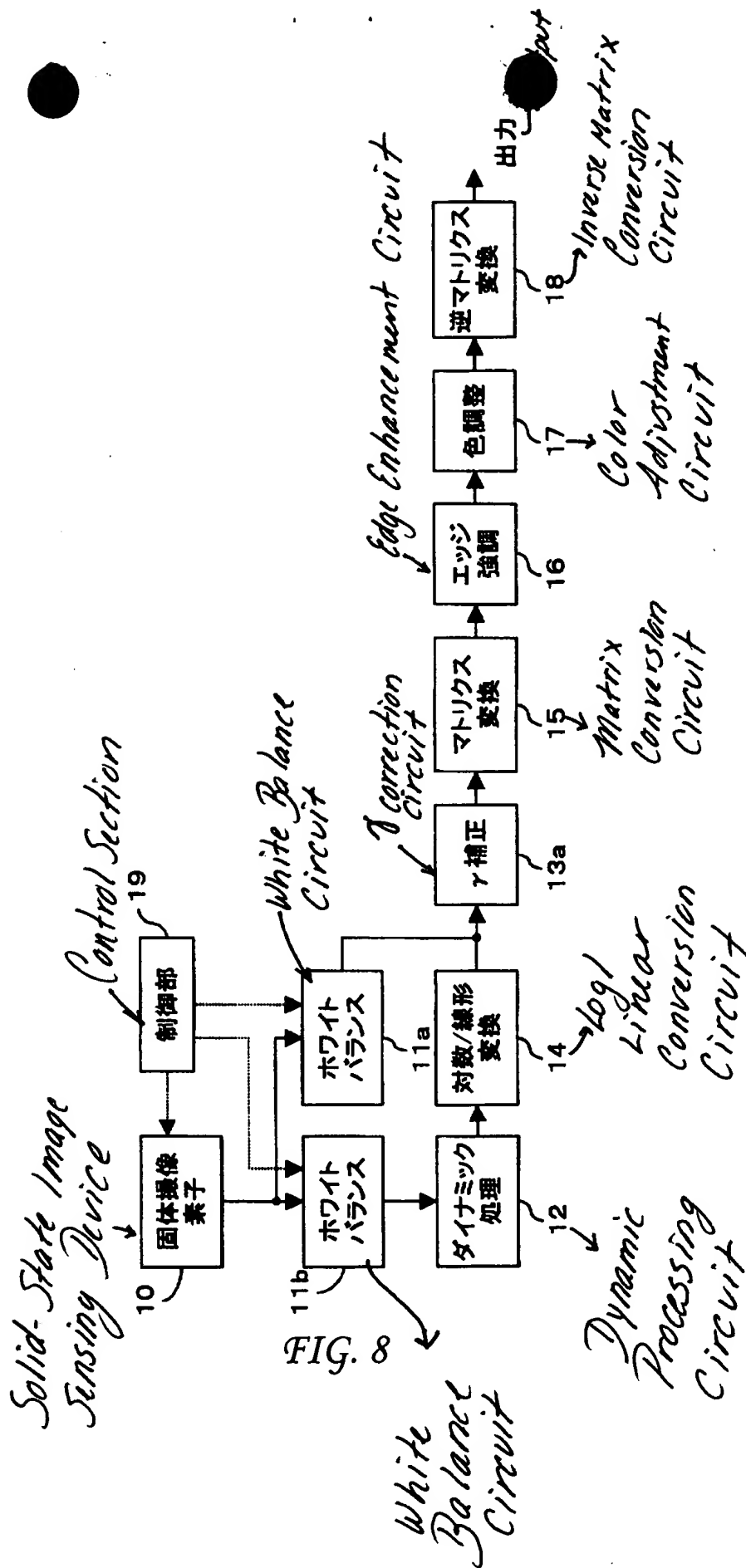
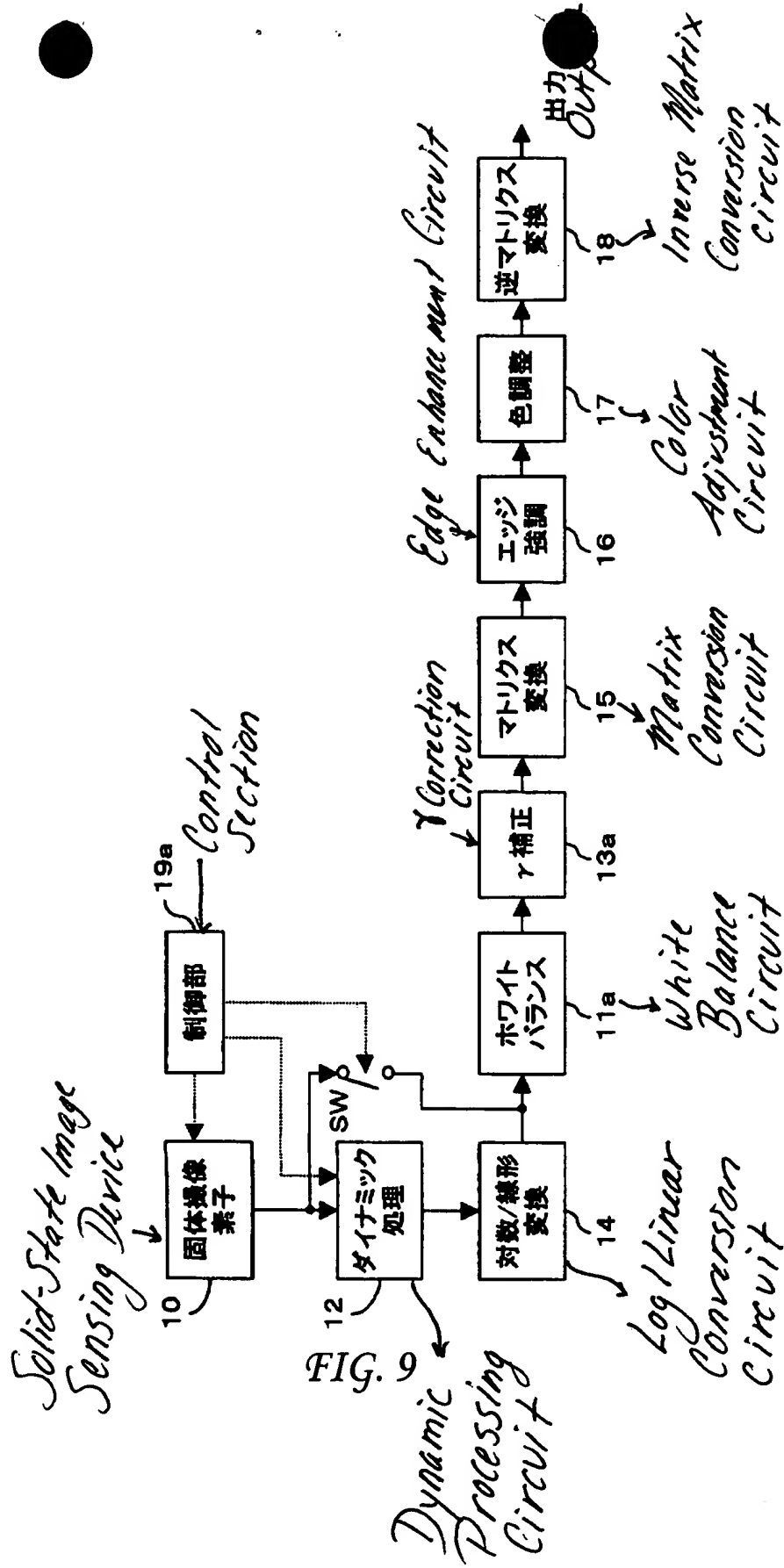


FIG. 6 \hookrightarrow Reset period







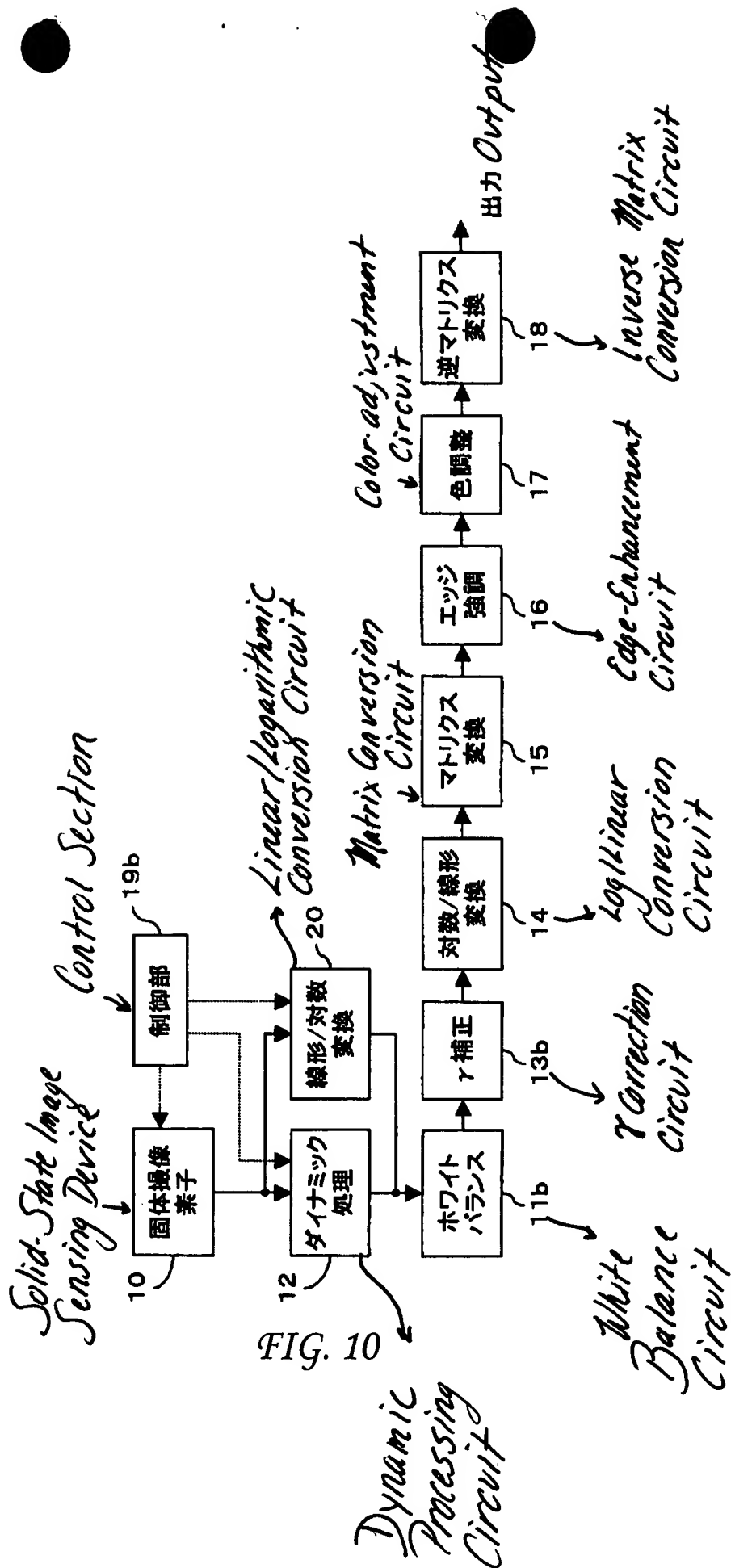
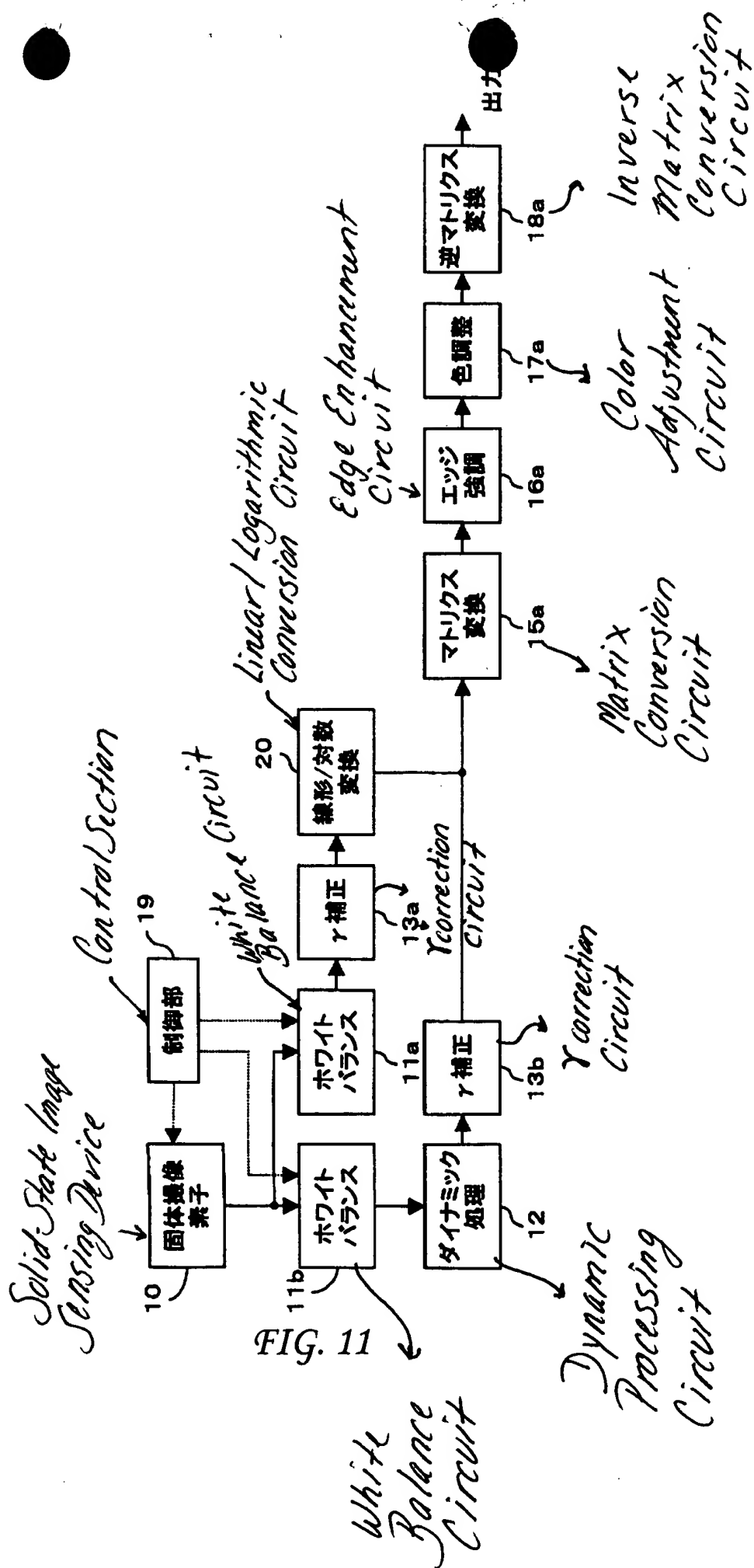
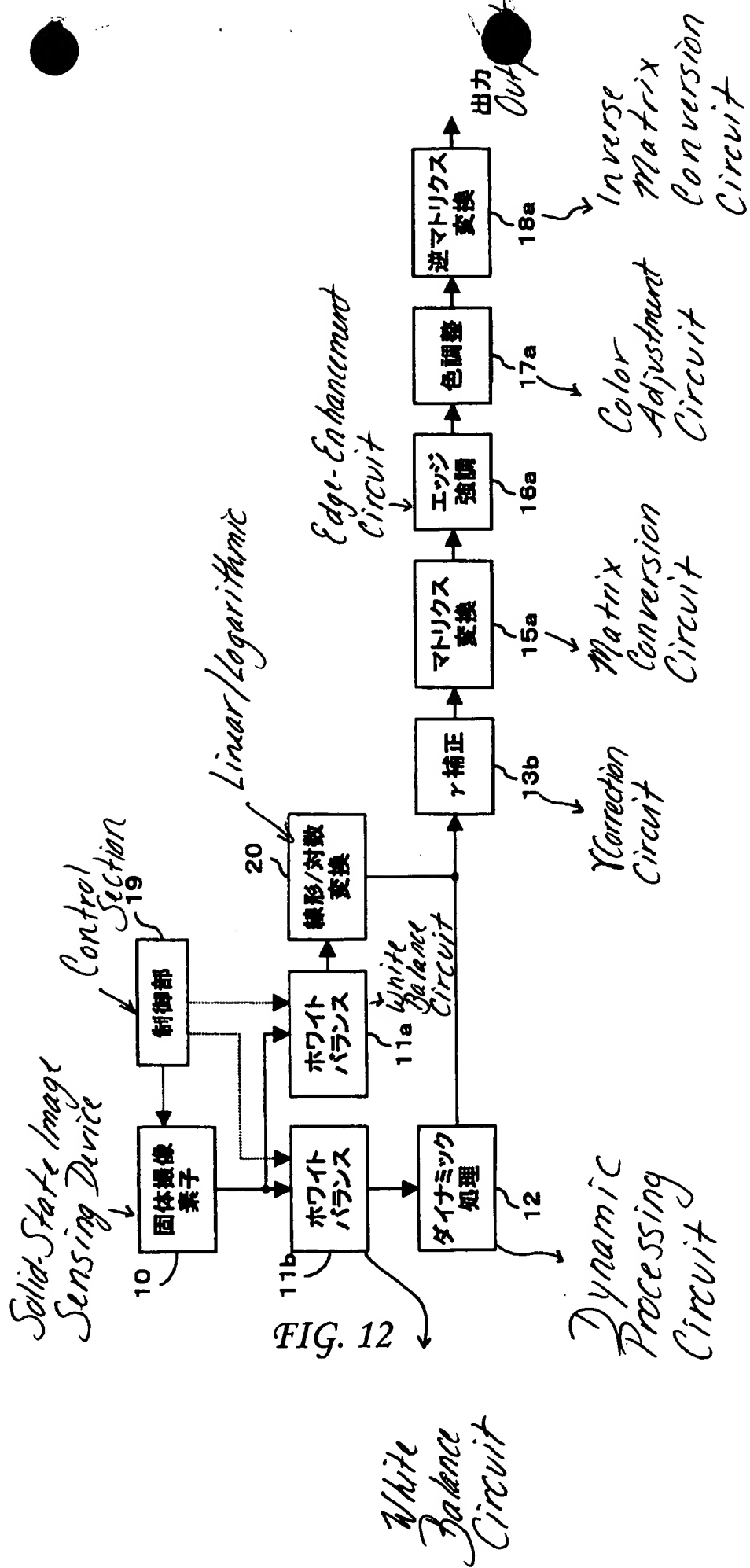


FIG. 10





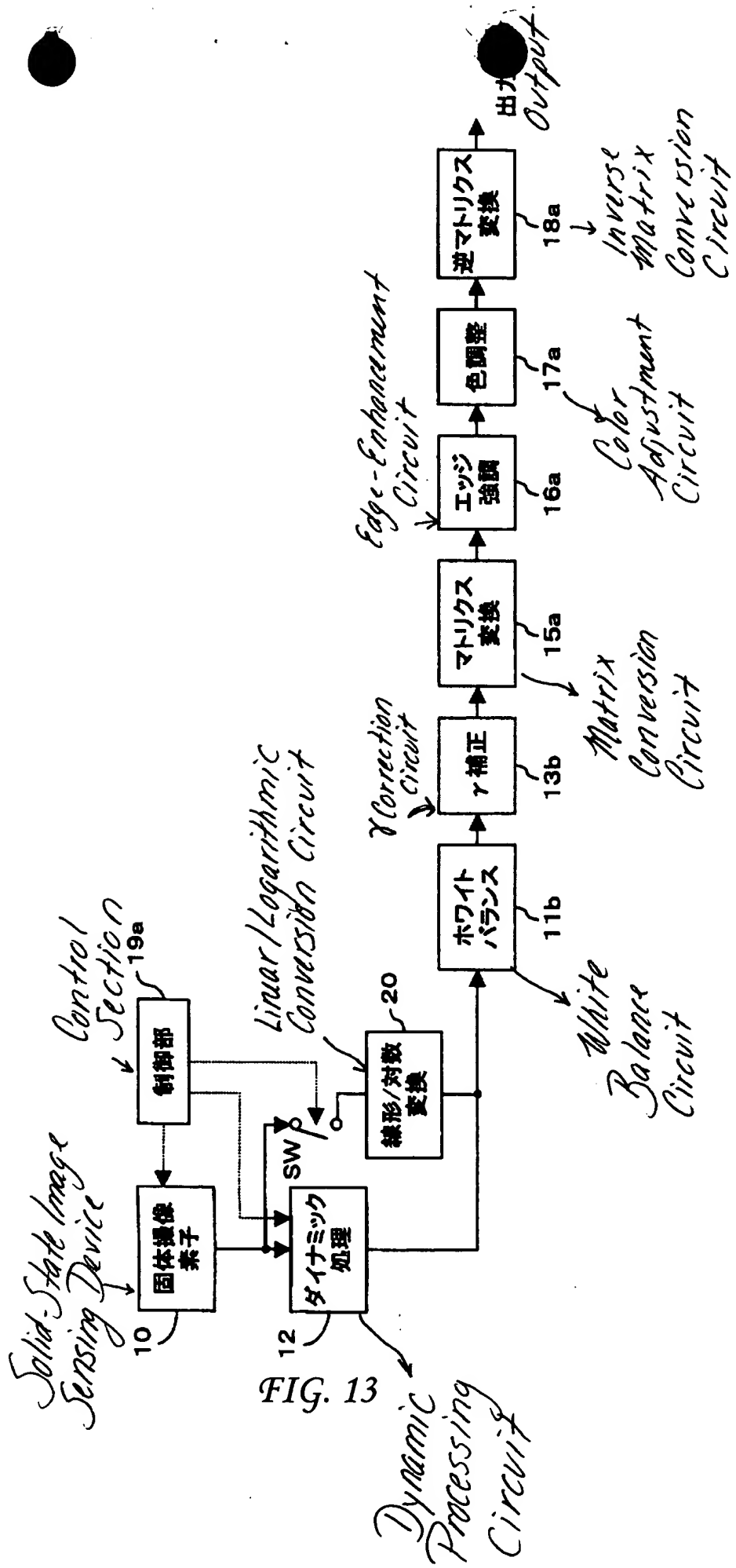


FIG. 13

